(19) World Intellectual Property Organization International Bureau



- | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 |

(43) International Publication Date 31 July 2003 (31.07.2003)

PCT

(10) International Publication Number WO 03/063213 A2

- (51) International Patent Classification⁷: H01L 21/18, 21/265, 21/762, 21/20, 21/76
- (21) International Application Number: PCT/EP03/00692
- **(22) International Filing Date:** 21 January 2003 (21.01.2003)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data: 0200813 23 January 2002 (23.01.2002) FR
- (71) Applicant (for all designated States except US):
 S.O.I.TEC SILICON INSULATOR TECHNOLOGIES [FR/FR]; Parc Technologique des Fontaines,
 Chemin des Franques, F-38190 Bernin (FR).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): GHYSELEN,
 Bruno [FR/FR]; 58, rue Georges Maeder, F-38170
 Seyssinet-Pariset (FR). LETERTRE, Fabrice [FR/FR];
 33, Quai Jongkind, F-38000 Grenoble (FR).
- (74) Agent: CABINET REGIMBEAU; Espace Performance, Bâtiment K, F-35769 Saint-Gregoire (FR).

- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declaration under Rule 4.17:

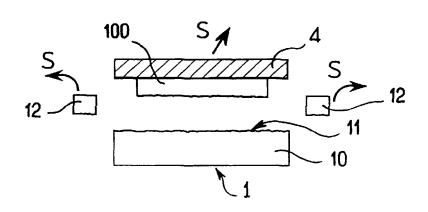
— of inventorship (Rule 4.17(iv)) for US only

Published:

 without international search report and to be republished upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: OPTIMIZED METHOD OF TRANSFER OF A THIN LAYER OF SILICON CARBIDE TO A RECEIVING SUBSTRATE



(57) Abstract: The invention relates to an optimized method of transfer of a thin layer (100) of monocrystalline silicon carbide, derived from a source substrate (1), to a receiving substrate (4), comprising the steps consisting of: - bombarding the front face of the said source substrate (1) with a majority of H+ ions to form an embrittlement zone (5), - detaching the said thin layer (100) from the remainder (10) of the said source substrate (1) along the said embrittlement zone (5), characterized in that the implantation of H+ ions is performed according to the following inequality, in which the implantation

dose D is expressed in number of H^+ ions/cm² and the implantation energy E, expressed in keV, is greater than or equal to 95 keV: $[(E \times 1.10^{14} + 5.10^{16})/1.1] \le D \le [(E \times 1.10^{14} + 5.10^{16})/0.9]$ and in that after the step of bonding, a thermal budget is applied sufficient to exfoliate completely or almost completely the zone (12) of the said thin layer (100) of the source substrate (1) which has not been transferred to the said receiving substrate (4).

O 03/063213 A2

1

OPTIMIZED METHOD OF TRANSFER OF A THIN LAYER OF SILICON CARBIDE TO A RECEIVING SUBSTRATE

The present invention relates to an optimized method of transfer of a thin layer of monocrystalline silicon carbide (SiC) derived from a source substrate of this same material to a receiving or stiffening substrate, this method particularly permitting recycling of the said substrate after the transfer of this thin layer.

The method known under the trademark "Smart Cut" enables a thin layer to be transferred from a source substrate to a receiving substrate of the type of oxidized silicon or polycrystalline silicon carbide, for example. This method likewise enables the source substrate from which the thin layer has been taken to be reused.

However, after each layer transfer operation, the upper surface of the source substrate has a certain number of surface irregularities.

The formation of these surface irregularities will be described with reference to the accompanying Figures 1-5, which show a specific example of implementation of the "Smart Cut" method. This method is known to those skilled in the art and will not be described in detail.

Figure 1 shows a source substrate 1 which has a planar face 2, termed "front face", on which a step of

WO 03/063213

PCT/EP03/00692

implantation of gas species has been performed. This implantation is carried out by ion bombardment, for example with H^{+} ions (reference B on Figure 1) using an implanter.

2

This implantation is performed at a specific energy, implantation dose, and temperature, so as to create an embrittlement zone, referenced 3, in the neighborhood of the mean implantation depth p of the ions.

This embrittlement zone 3 delimits two portions in the said source substrate 1, i.e., on the one hand, an upper thin layer 100 extending between the front face 2 and the said embrittlement zone 3, and on the other hand, the remainder of the substrate, referenced 10.

As shown in Figure 2, a stiffener or receiving substrate 4 is then applied to the front face 2 of the source substrate 1.

This stiffener is chosen by those skilled in the art as a function of the final application envisaged. In a known manner, it can be applied, for example, by evaporation, spraying, chemical vapor deposition, or can be bonded using an adhesive or by a technique of "bonding by molecular adhesion" known in English terminology as "wafer bonding".

As shown in Figure 3, a step is then carried out of detachment of the thin layer 100 from the remainder 10 of the substrate 1. This detachment step, symbolized by the arrows S, is performed either by application of mechanical stresses to the stiffener 4, or by the application of thermal energy to the assembly of stiffener 4 and substrate 1.

The wafers constituting the source substrate 1 used for molecular adhesion possess subsided edges due to

3

chamfering operations performed, for example, during their manufacture. As a result, the adhesion forces between the stiffener 4 and the front face 2 are weaker in the region of the substantially annular periphery of the said source substrate 1.

Consequently, during the stripping of this stiffener 4, only the central portion of the thin layer 100, strongly adhering to the stiffener 4, is detached, while the substantially annular periphery of this thin layer 100 remains integral with the remainder 10 of the source substrate 1.

This source substrate 1 thus simultaneously presents, in its central portion, a surface roughness 11 due to the rupture in the region of the embrittlement zone 3, and at its periphery, an excess thickness 12 or surface topology in the form of a blistered zone corresponding to zones not transferred to the receiving substrate or stiffener 4.

The depth of this zone of excess thickness 12 is equal to the thickness of the transferred thin layer 100. It typically varies from several tens of nanometers to more than a micrometer. This depth is determined by the implantation energy of the hydrogen ions.

In the Figures, the zone 12 of excess thickness has been intentionally shown, for the sake of simplification and schematization, with a cross section with right angles and with a marked thickness with respect to the remainder 10 of the source substrate. In reality, it has a much more irregular shape and a proportionally smaller thickness.

Before proceeding to the transfer of the following thin layer, it is imperative to recycle the remainder 10 of the source substrate.

4

This recycling consists, on the one hand, of a step of planarization of the surface (arrows P), that is, of elimination of the excess thickness 12 and of the subsided zones, and on the other hand, of a specific finishing step (arrow F) permitting elimination of the surface roughness 11 to attain a substrate having a new front face 2'.

These recycling steps are generally performed by mechanical and/or mechano-chemical polishing techniques.

In the specific case where the source substrate 1 is silicon carbide, an extremely hard material, these polishing steps are found to be extremely long and costly.

The prior art document, "The effects of damage on hydrogen-implant-induced thin-film separation from bulk silicon carbide", R.B. Gregory, Material Research Society Symp., Vol. 572, 1999, shows that the choice of the conditions of hydrogen implantation into the silicon carbide permits varying the percentage of the zone exfoliated, that is, the percentage of the free surface of silicon carbide which is spontaneously eliminated during the thermal annealing of the substrate.

In this article, the curve of results showing the H+ion implantation dose as a function of the percentage of the zone exfoliated possesses a bell shape at an implantation energy of 60 keV, with a maximum value of 33% of the exfoliated zone, for an implantation dose of $5.5 \cdot 10^{16}$ H⁺/cm². On departing from this value, that is, on increasing or decreasing the implantation dose, this exfoliation percentage decreases.

The document "Complete surface exfoliation of 4H-SiC by H^+ and Si+ co-implantation", J.A. Bennett, Applied Physics Letters, Vol. 76, No. 22, pp. 3265-3267, May 29,

5

2000, shows that it is possible to perform a complete exfoliation of the surface of a silicon carbide substrate by a co-implantation of H^+ and Si^+ ions.

More specifically, this document describes trials performed on 4H-SiC silicon carbide by implanting Si+ions at various doses and at an energy of 190 keV, then H⁺ ions at an implantation dose of $6\cdot 10^{16}$ H⁺/cm² and at an energy of 60 keV. Implantation doses of Si+ greater than or equal to $5\cdot 10^5$ Si⁺/cm² permitted exfoliation of 100% of the silicon carbide surface.

However, the Si ion implantation dose necessary for the total exfoliation of the SiC layer is also high enough to render the silicon carbide amorphous. This method is therefore incompatible with the transfer of a thin film of silicon carbide of good crystalline quality, since it is unimaginable to recover this thin film for the later formation of devices used in microelectronics or opto-electronics.

The object of the invention is to remedy the said disadvantages and particularly to optimize the implantation conditions to facilitate the recycling of a silicon carbide source substrate.

This object is attained by means of a method of transfer of a thin layer of monocrystalline silicon carbide, derived from a source substrate of the same material, to a receiving substrate, this method being intended to facilitate the recycling of the remainder of this source substrate, comprising the steps consisting of:

- bombarding a planar face, termed "front face", of the said source substrate with a majority of H^+ ions to form an embrittlement zone at a depth near the mean

implantation depth p of the said ions, this zone forming a delimitation between the said thin layer and the remainder of the said substrate, the bombardment with the H^+ ions being performed with an implantation energy E and an implantation dose D.

- bonding the said receiving substrate to the said front face,
- detaching the said thin layer from the remainder of the source substrate, along the said embrittlement zone.

According to the characteristics of the invention, the implantation of the H⁺ ions is performed in accordance with the following inequality, in which the implantation dose D is expressed in a number of H⁺ ions per cm2 and the implantation energy E, expressed in keV, is greater than or equal to 95 keV:

$$[(E \times 1 \cdot 10^{14} + 5 \cdot 10^{16})/1.1] \le D \le [(E \times 1 \cdot 10^{14} + 5 \cdot 10^{16})/0.9]$$

so as to embrittle the said embrittlement zone in an optimized manner, and after this step of bonding, a thermal budget is applied sufficient to completely or almost completely exfoliate the zone of the said thin layer of the source substrate which has not been transferred to the said receiving substrate.

According to other advantageous but non-limiting characteristics of the invention, taken alone or in combination:

- the implanted ions are exclusively H' ions;
- the implantation of H^+ ions is performed without intentional heating of the source substrate during the exposure to the ion beam;

7

WO 03/063213 PCT/EP03/00692

- the source substrate is of disoriented monocrystalline silicon carbide;

- the detachment is first performed along the said optimized embrittlement zone by the application of external mechanical stresses, and the sufficient thermal budget is then applied for completely or almost completely exfoliating the zone of the thin layer which has not been transferred to the receiving substrate, or else:
- the detachment along the optimized embrittlement zone and the complete or almost complete exfoliation of the zone of the thin layer which has not been transferred to the receiving substrate, are performed simultaneously by the application of the appropriate thermal budget;
- the application of the thermal budget necessary for completely or almost completely exfoliating the said zone of the said thin layer zone takes place above 700°C;
- the ion bombardment is effected in a random fashion;
- the source substrate is covered with a layer of amorphous material of a thickness less than or equal to about 50 nanometers (50 nm), before the step of ion bombardment;
- the bonding of the receiving substrate to the front face of the source substrate is performed by molecular adhesion;
- at least one of the faces, among the front face and the contact face of the receiving substrate, is covered with an intermediate bonding layer;
- the said layer of amorphous material and/or the said intermediate bonding layer are formed of a material

chosen from among silicon oxide (SiO_2) or silicon nitride (Si_3N_4) ;

- the receiving substrate is chosen from among silicon, silicon carbide, gallium nitride, aluminum nitride, sapphire, indium phosphide, gallium arsenide, or germanium;
- the receiving substrate is silicon obtained by zone fusion growth, with a low oxygen content;
- the method furthermore comprises a step of finishing the front face of the source substrate obtained after detachment;

The invention will now be described with reference to an embodiment of the invention, given as a purely illustrative example, this description being based on the accompanying drawings, in which:

- figures 1-5 are diagrams showing the different steps of a method of transfer of a thin silicon carbide layer according to the state of the art,
- figures 6-18 are diagrams showing the different steps of the method according to the invention and its alternatives,
- figure 19 is a graph showing the exfoliated zone percentage as a function of the H^{+} ion implantation dose D, at various implantation energies, and
- figure 20 is a graph showing the values of H^{\star} ion implantation energy E as a function of the H^{\star} ion implantation dose D to obtain a 100% exfoliation of the implanted layer.

The method according to the invention will now be described with reference to Figures 6-18.

Figures 6-18 are closely similar to Figures 1-5, and identical elements bear the same reference numerals.

WO 03/063213

9

PCT/EP03/00692

It can be seen in Fig. 6 that the object of the invention is to optimize the implantation conditions B of atomic species within а source substrate monocrystalline silicon carbide, so as to create in the neighborhood of the means depth p of ion implantation, a zone 5, termed "optimized embrittlement zone", to later be able to exfoliate, after the detachment of the layer 100 and after having applied an appropriate thermal budget, 100% or about 100% of the blistered excess thickness zone 12 which remained integral with the remainder 10 of the substrate in the prior art method (see Figure 3).

The term "optimized embrittlement" means that atomic species are introduced into the crystal in a precise and controlled manner in order to activate in an optimum manner the embrittlement mechanisms which are used.

Within the scope of the invention, the implantation of atomic species consists of an ion bombardment of the front face 2 of the substrate 1 with H^{+} ions, and possibly joint bombardment with H^{+} ions and helium or boron ions, the H^{+} ions nevertheless remaining in the majority.

The implantation of these ions in the surface of the monocrystalline silicon carbide source substrate 1 is performed using an ion beam implanter.

Various trails of implantation in the surface of a silicon carbide source substrate 1 were performed in order to determine the best operating conditions.

As illustrated in the accompanying Figures 7 and 8, which show the crystalline structure of silicon carbide, the implantation B of ions is always performed

10

perpendicularly to the planar surface 2 of the source substrate 1.

In the case illustrated in Figure 7, corresponding to an oriented silicon carbide crystal (known to those skilled in the art by the English term "on axis"), the stackings of silicon and carbon atoms are situated in a plane parallel to the upper planar surface or front face 2. The ions then penetrate parallel to the crystal growth axis C of the said crystal.

On the contrary, in the case illustrated in Figure 8, corresponding to a disoriented silicon carbide crystal (known to those skilled in the art by the English term "off axis"), the stackings of silicon and carbon atoms are situated in a plane which is not parallel to the front face 2 but which forms an angle a with the latter. The ions then penetrate along an axis perpendicular to the said front face 2 but angularly offset from the said crystal growth axis C by the value of this angle a.

The disorientation of the crystal is obtained in an artificial fashion, generally by cutting the crystal. The angle a can have different values. It is 3.5°, 8°, or 15° in currently commercially available crystals of silicon carbide.

In the two said cases, the microcavities 50 resulting from ion implantation are mainly formed in a plane perpendicular to the crystalline growth axis C.

Consequently, in the oriented crystal, the microcavities 50 are parallel to the front face 2 and form an optimized embrittlement zone 5 parallel to the plane of the said front face 2 and occurring as a practically continuous fracture line, while in a disoriented crystal, these microcavities 50 are inclined

with respect to the plane of the front face 2 and do not form a continuous line.

It has been observed experimentally that for a given implantation dose D and energy E, a greater embrittlement of the zone 5, and later a better exfoliation percentage of the excess thickness zone 12, is obtained when the implantation is performed on a disoriented crystal.

In an advantageous manner, in the method of the invention, this ion implantation is performed in a random fashion, to avoid the phenomenon termed "channeling".

The channeling phenomenon occurs when ions are implanted along specific channels or crystallographic axes. The ions thus introduced encounter fewer atoms on their trajectories, resulting in fewer interactions and less braking of the said ions. These ions will therefore be implanted more deeply.

In the process according to the invention, it is on the contrary sought to avoid this channeling phenomenon for industrial reasons, because channeled implantation is not an industrially feasible technique.

Furthermore, in order to improve the percentage of exfoliation finally obtained during detachment, and to operate in a random implantation mode, it is preferable (but not obligatory) to perform ion implantation through a layer of amorphous material 20 formed on the front face 2 of the substrate 1. This alternative embodiment is shown in Figure 9.

This layer of amorphous material 20 is advantageously a layer of silicon oxide (SiO_2) or of silicon nitride (Si_3N_4) . However, it should not exceed a thickness of about 50 nm $(5\cdot 10^{-9}$ m), at the risk of modifying the relationship connecting the implantation

energy E and the implanted dose D as will be defined later.

Finally, the trials performed showed that this ion implantation should preferably be preformed without external heat supply, that is, at a maximum temperature of about 200°C, the implantation being able to bring the silicon carbide substrate substantially to this temperature.

If the substrate is heated during implantation, for example to temperatures markedly greater than 650°C, degradation or suppression of the macroscopic exfoliation phenomenon is observed. This can be explained by the implantation of activation during diffusion and recrystallization mechanisms which participate in the This activation in situ, which process. is not controlled, degrades the control of the desired exfoliation effect.

Other, complementary trials were performed in order to determine the best pairs of implantation dose D and implantation energy E to be used. These trials will later be described in detail.

The receiving substrate 4 is then bonded to the front face 2 of the source substrate 1.

The bonding of this substrate 4 is advantageously effected by molecular adhesion, since this is the mode of bonding best suited to electronic applications, particularly due to the homogeneity of the bonding forces used and to the possibility of bonding together numerous materials of different natures. However, it could likewise be performed by other known prior art techniques.

The bonding can be performed by applying one of the faces 40, termed "contact face", of the receiving

substrate 4 to the front face 2 of the source substrate 1, either directly (see Figure 10) or by means of one or more intermediate bonding layers 6 deposited on the contact face 40 of the substrate 4 (as shown in Figure 11), or on the front face 2 of the source substrate 1, or on these two faces.

These intermediate bonding layers 6 are insulating layers, for example, of silicon oxide (SiO_2) or silicon nitride (Si_3N_4) , or on the other hand, conductive layers.

It will be noted that when the implantation B is performed through a layer of amorphous material 20, the latter can then play the part of intermediate bonding layer 6.

The assembly of layers respectively illustrated in Figures 12 and 13 is obtained.

The receiving substrate is advantageously (but not obligatory) chosen from silicon, silicon carbide, gallium nitride (GaN), aluminum nitride (AlN), sapphire, indium phosphide (InP), gallium arsenide (GaAs), or germanium (Ge). Other materials can likewise be used.

This receiving substrate can likewise be a fragile substrate, such as silicon obtained by zone fusion growth, with a low oxygen content, that is, monocrystalline silicon obtained by the zone fusion method starting from polycrystalline silicon.

Detachment (arrows S) of the receiving substrate 4 and of the thin layer 100 from the remainder 10 of the source substrate 1 is then performed.

This detachment S can be performed either by application of external mechanical stresses (as shown in Figure 14), or by a thermal treatment with an appropriate thermal budget (see Figure 17).

14

WO 03/063213 PCT/EP03/00692

In a fashion known to those skilled in the art, these external mechanical stresses can be, for example, the application of shear, traction, or compression forces, or the application of ultrasound, or of an electrostatic or electromagnetic field.

If the detachment S is performed using external mechanical stresses, an annealing treatment then follows, according to an appropriate thermal budget to completely, or almost completely, exfoliate the zone 12 of the thin layer 100 from the source substrate 1 which has not been bonded to the receiving substrate 4 (see Figure 15). The thermal budget corresponds to the product of the annealing treatment temperature by the duration of this treatment.

This treatment is nevertheless simpler and quicker to use than the polishing used in the state of the art for proceeding to such an operation.

There then only remains a possible finishing step F of the source substrate 1 (see Figure 16).

This finishing step aims at eliminating the surface roughness 11. It is generally performed by a mechanochemical polishing.

A step of finishing the free surface of the thin layer 100 can likewise be performed.

If the detachment S of the thin layer 100 from the remainder 10 of the source substrate 1 is performed by a thermal annealing treatment (see Figure 17), the complete or almost complete exfoliation of the zone 12 is then simultaneously produced (see Figure 18). In practice, this application of heat causes the defects or microcavities 50 to grow until they form micro-cracks 51

which, connected together, result in a cleavage plane and in rupture along the said optimized embrittlement zone 5.

The possible finishing step F is then performed, as illustrated in Figure 16.

The trials to determine the best pairs of values of implantation dose D and implantation energy E of H^{+} ions will now be described.

Operating conditions:

H⁺ ion implantation was performed on the front face of a monocrystalline silicon carbide SiC4H substrate disoriented by 8° (known to those skilled in the art as "SiC4H 8° off-axis"), covered with an amorphous layer of silicon oxide about 50 nanometers (50 nm) thick.

The implantation temperature was below 200°C.

Four series of trials were performed with implantation energies E of respectively 50 keV, 95 keV, 140 keV and 180 keV.

For each value of energy E, the implantation dose D was varied between $5.25\cdot10^{16}~{\rm H}^+/{\rm cm}^2$ and $8\cdot10^{16}~{\rm H}^+/{\rm cm}^2$.

A receiving substrate 4 constituted by silicon was then applied to the source substrate 1, and annealing was then performed at 900°C for 1 hour.

The percentage exfoliation of the peripheral zone 12, which in the prior art remained integral with the remainder 10 of the substrate, was then measured.

The results obtained are given in the following Table.

A value of 100% means that the whole of the zone 12 which is not in intimate contact (that is, has not adhered) with the receiving substrate 4 is exfoliated. A value of 30%, for example, means that only 30% of the excess thickness zone 12 is exfoliated, and that 70% of

this zone 12 remains integral with the remainder 10 of the substrate 1.

		Implantation dose D (10 ¹⁶ H ⁺ /cm ²)									
	Impl-										
	anta-	5.25	5.5	5.75	6	6.5	6.75	7	7.25	7.5	8
	tion										
	Energy										
	E										
	(keV)										
Exfol-	50	5	50	15							
iated Zone (%)	95		10		100	60		30			
	140		-	_	40	80	100	60		40	
	180					30		90	100	80	40

Trials carried out with implantation energies less than 50 keV did not give good results for exfoliation.

The results obtained have been transferred to the graph of Figure 19. It can be seen that for each implantation energy E value greater than or equal to 95 keV, there is an implantation dose value D which enables 100% of exfoliation to be obtained (bell-shaped curves obtained).

Figure 20 is a graph showing the H+ ion implantation dose D as a function of the implantation energy E of these same ions to obtain 100% of exfoliated zone.

The straight line obtained corresponds to the following equation:

$$D = E \times 1 \cdot 10^{14} + 5 \cdot 10^{16} \tag{1}$$

with $E \ge 95 \text{ keV}$,

in which the H^+ ion implantation energy E is expressed in keV and the implantation dose D of these ions is expressed in H^+ ions/cm².

Taking account of the fluctuations of the values of D and E connected to possible slight experimental variations and to the tolerances of manufacture and control of the implantation apparatus used, it is considered that the pairs of values of D and E should obey the following inequality:

$$-\epsilon \times D \leq D - (E \times 1.10^{14} + 5.10^{16}) \leq \epsilon \times D \tag{2}$$

where D and E have the aforementioned meanings and where $\varepsilon \times D$ represents the absolute tolerance for a given value of E between the theoretical value of D obtained according to the above Equation (1) and an acceptable value of D, and where ε represents the relative tolerance.

After experimentation, it was considered that this relative tolerance c was equal to 10%.

The following inequality (3) results from this:

$$-0.1 \times D \le D - (E \times 1.10^{14} + 5.10^{16}) \le 0.1 \times D$$

which can also be expressed as the following inequality (4):

$$[(E \times 1.10^{14} + 5.10^{16})/1.1] \le D \le [(E \times 1.10^{14} + 5.10^{16})/(0.9)]$$

The pairs of values of D and E which obey the above inequality (4) permit an optimized embrittlement of the embrittlement zone 5 to be obtained, and after having applied a sufficient thermal budget, permits complete or

18

almost complete exfoliation of the zone 12 of the thin layer 100 which has not been transferred to the receiving substrate 4.

Supplementary trials were then performed to determine the appropriate thermal budget.

Below 700°C, the mechanisms of diffusion of hydrogen within the SiC are practically inoperative.

It was thus possible to determine that the thermal budget necessary for complete or almost complete exfoliation of the zone 12 has to be above 700°C and preferably above 800°C.

There was thus obtained, on the one hand, a thin layer 100 of SiC, of good crystalline quality, and on the other hand, a source substrate 1, the front face of which is free from surface topologies 12.

19

WO 03/063213 PCT/EP03/00692

CLAIMS

1. Method of transfer of a thin layer (100) of monocrystalline silicon carbide, derived from a source substrate (1) of the same material, to a receiving substrate (4), this method, intended to facilitate the recycling of the remainder (10) of this source substrate (1), comprising the steps consisting of:

- bombarding a planar face (2), termed "front face", of the said source substrate (1) with a majority of H⁺ ions to form an embrittlement zone (5) at a depth near the mean implantation depth p of the said ions, this zone (5) forming a delimitation between the said thin layer (100) and the remainder (10) of the said substrate (1), the bombardment with the H⁺ ions being performed with an implantation energy E and an implantation dose D.

- bonding the said receiving substrate (4) to the said front face (2),
- detaching the said thin layer (100) from the remainder (10) of the said source substrate (1), along the said embrittlement zone (5),

characterized in that the implantation of H^+ ions is performed according to the following inequality, in which the implantation dose D is expressed in number of H^+ ions/cm² and the implantation energy E, expressed in keV, is greater than or equal to 95 keV:

$$[(E\times 1\cdot 10^{14} + 5\cdot 10^{16})/1.1] \le D \le [(E\times 1\cdot 10^{14} + 5\cdot 10^{16})/0.9]$$

so as to embrittle the said embrittlement zone (5) in an optimized fashion, and in that after the step of bonding, a thermal budget is applied sufficient to

completely or almost completely exfoliate the zone (12) of the thin layer (100) of the source substrate (1) which has not been transferred to the said receiving substrate (4).

- 2. Method according to claim 1, characterized in that the implanted ions are exclusively H^+ ions.
- 3. Method according to claim 1 or 2, characterized in that the implantation of H^{+} ions is performed without intentional heating of the source substrate (1) during the exposure to the ion beam.
- 4. Method according to any one of the foregoing claims, characterized in that the source substrate (1) is disoriented monocrystalline silicon carbide.
- 5. Method according to any one of the foregoing claims, characterized in that detachment is first performed along the said optimized embrittlement zone (5) by the application of external mechanical stresses and in that the sufficient thermal budget is then applied for completely or almost completely exfoliating the zone (12) of the thin layer (100) which has not been transferred to the receiving substrate (4).
- 6. Method according to any one of claims 1-4, characterized in that the detachment along the optimized embrittlement zone (5) and the complete or almost complete exfoliation of the zone (12) of the thin layer (100) which has not been transferred to the receiving substrate (4) are simultaneously performed by the application of the appropriate thermal budget.
- 7. Method according to any one of the foregoing claims, characterized in that the application of the thermal budget necessary for completely or almost

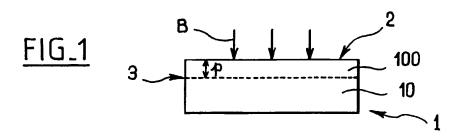
completely exfoliating the said zone (12) from the thin layer (100) is effected above 700°C.

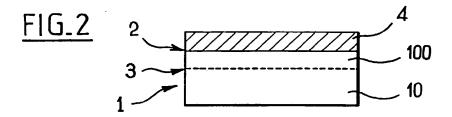
- 8. Method according to any one of the foregoing claims, characterized in that the ion bombardment is performed in a random fashion.
- 9. Method according to any one of the foregoing claims, characterized in that the source substrate (1) is covered with a layer of amorphous material (20) of thickness less than or equal to about 50 nanometers, before the step of ion bombardment.
- 10. Method according to any one of the foregoing claims, characterized in that the bonding of the receiving substrate (4) to the front face (2) of the source substrate (1) is performed by molecular adhesion.
- 11. Method according to claim 10, characterized in that at least one of the faces between the front face (2) and the contact face (40) of the receiving substrate (4) is covered with an intermediate bonding layer (6).
- 12. Method according to claim 9 or 11, characterized in that the said layer of amorphous material (20) and/or the said intermediate bonding layer (6) are formed of a material chosen from among silicon oxide (SiO_2) or silicon nitride (Si_3N_4).
- 13. Method according to any one of the foregoing claims, characterized in that the receiving substrate (4) is chosen from among silicon, silicon carbide, gallium nitride, aluminum nitride, sapphire, indium phosphide, gallium arsenide, or germanium.
- 14. Method according to any one of the foregoing claims, characterized in that the receiving substrate (4) is silicon obtained by zone melting growth, with a low oxygen content.

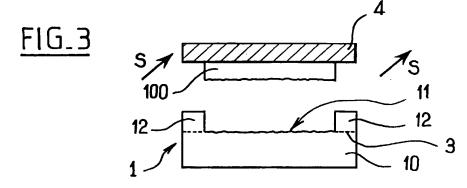
22

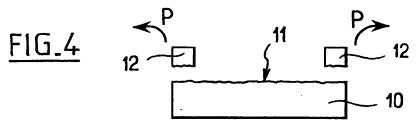
15. Method according to any one of the foregoing claims, characterized in that it furthermore comprises a finishing step F of the front face (11) of the remainder (10) of the source substrate (1) obtained after detachment.













2/5

